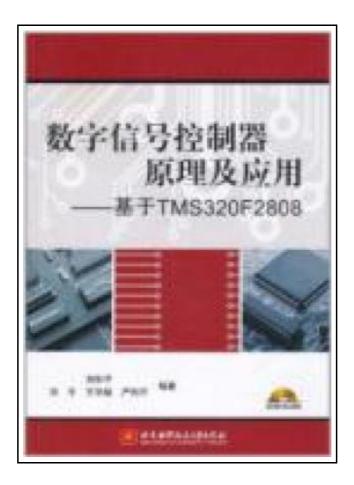
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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 423 Publisher: Beijing University of Aeronautics Pub. Date: 2011-07-01 version 1. Introduction TI has introduced digital signal controller chip TMS320F. 2808 of the hardware structure, the internal function modules, system control and interrupt lines, addressing modes, assembly language. C language programming and debugging environment. F2808 experimental development board. peripheral module. Liu and equal principle and application of digital signal controller - based on TMS320F2808 to F2808 principles and applications of the main line. introducing various functional modules (IO. eSCI. AD. ePWM. eQEP. eCAP. I2C. SPI. eCAN) of basic principles. and describes the corresponding application examples are given of its application of the circuit diagram and the source list. The book provides all of the programs are designed in the author's experiments supporting the development board through debugging. Book easy to understand. easy to use clear examples. sought to learn digital signal controller application development staff easy to get started. get started quickly. With CD-ROM. containing all the code book. Principle and Application of digital signal controller - based on TMS320F2808 in digital signal controllers as application development engineers and technicians of the reference book. but also as a university electronic information. automation. computers and other undergraduates. graduate students. Digital Signal control theory and applications course materials. Contents: Chapter 1 TMS320F2808 Overview 1.1 Overview 1.2 TMS320F2808 CPU controller functional block diagram 1.3 TMS320F2808 pin Features 1.4 TMS320F2808 memory maps in Chapter 2 TMS320F2808 internal memory bus functions 2.1 2.3 2.2 peripheral bus real-time JTAG2.4 storage interrupt expansion module unit 2.5 2.7 2.6 external interrupt oscillator with PLL 2.8 2.9 peripheral module clock program monitor lowpower mode 2.10 2.11 2.12 General structure of peripheral input and output multiplexer...

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